

IN THE CLAIMS:

The claims are amended as follows:

D1
B2

1. (Amended) A method for generating ATM cells for low bit rate applications, said method comprising:

scheduling ATM cell transmission times in a way as to keep ATM cell spacing as constant as possible, and

multiplexing a plurality of low bit rate connections into a same ATM connection having the thus scheduled ATM cell transmission times.

2. (Amended) A method according to claim 1, wherein said ATM cell spacing is kept as close as possible to a cell rate negotiated for the corresponding ATM connection.

D1 B3

6. (Amended) A method according to claim 2, wherein said cell rate may be renegotiated.

D1 B4

11. (Twice Amended) A device for generating ATM cells for low bit rate applications, said device comprising:

means for scheduling ATM cell transmission times in a way as to keep ATM cell spacing as constant as possible, and

D
B4
cont.
AMENDMENT UNDER 37 C.F.R. § 1.111
U.S. Patent Application No. 09/429,028

means for multiplexing a plurality of low bit rate connections into a same ATM
connection having the thus scheduled ATM cell transmission times.
